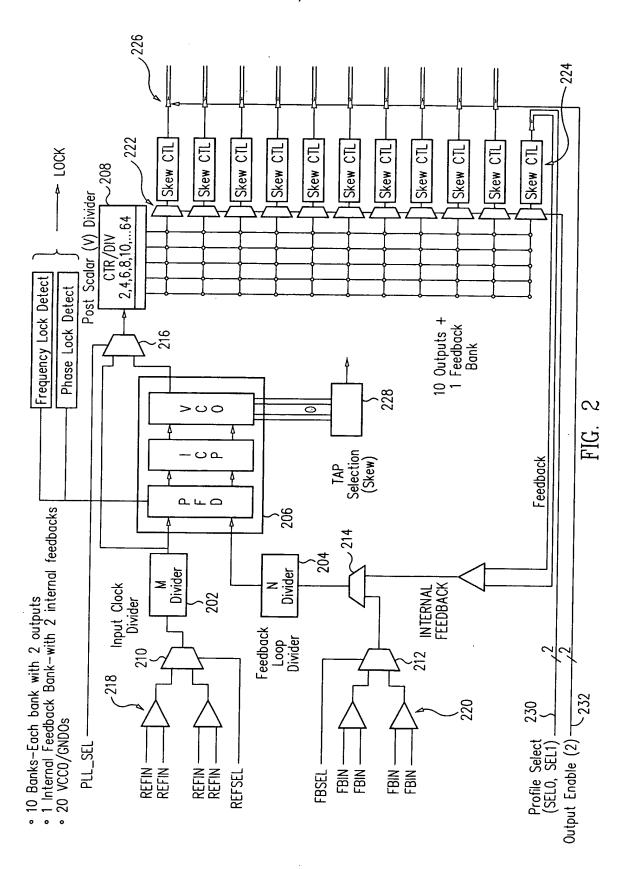


Clock Generator With Skew Control
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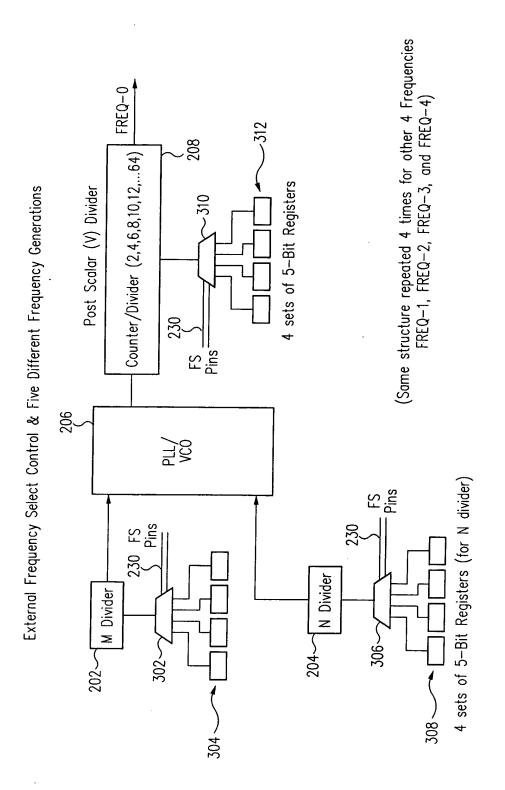
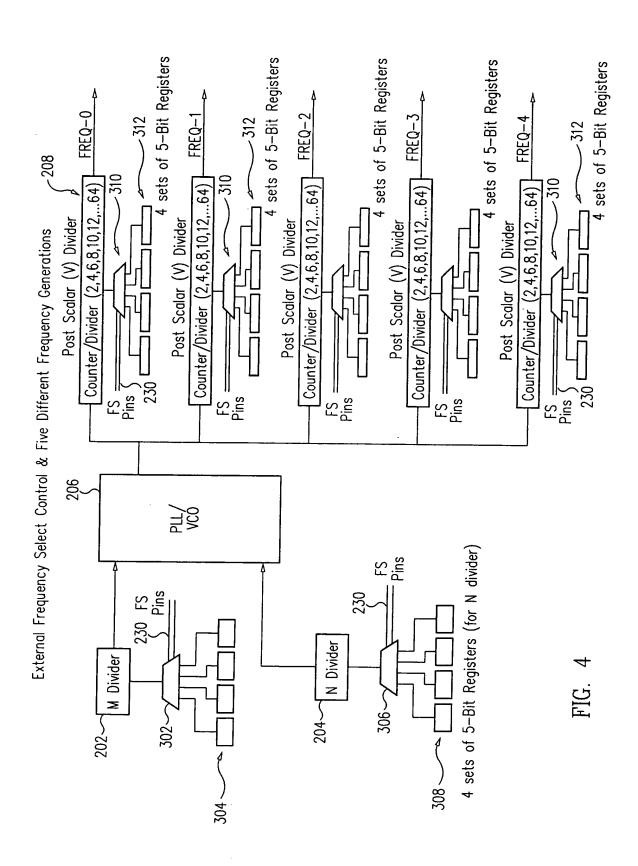


FIG. 3



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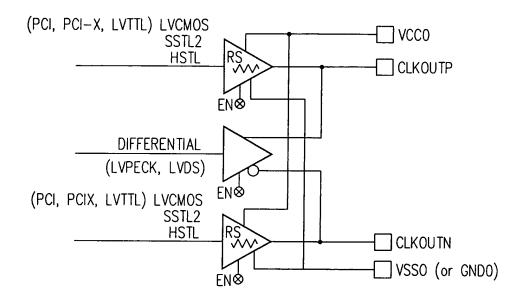


FIG. 5

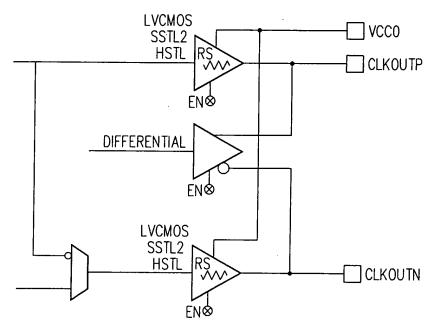
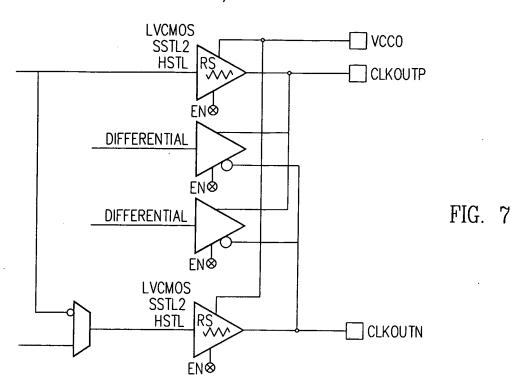
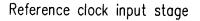


FIG. 6

Clock Generator With Skew Control
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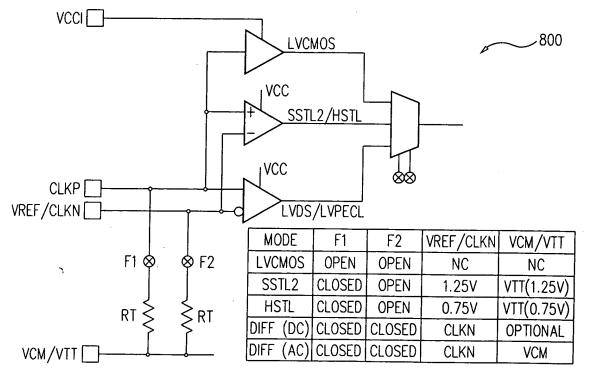
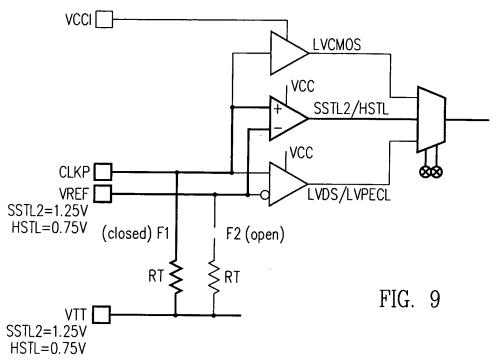


FIG. 8

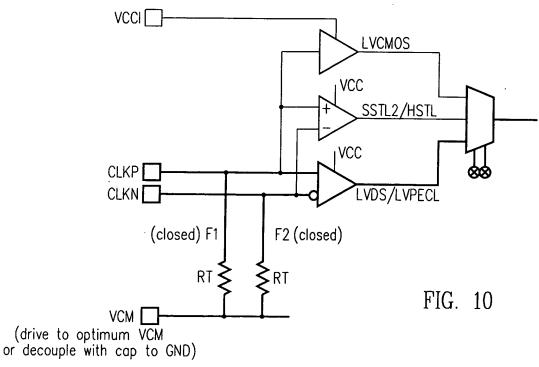
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SSTL2/HSTL input application



Alternative DC coupled differential application



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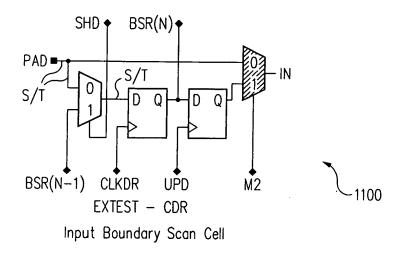


FIG. 11

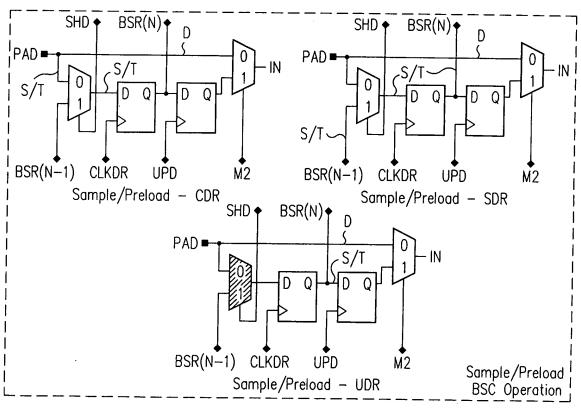


FIG. 12

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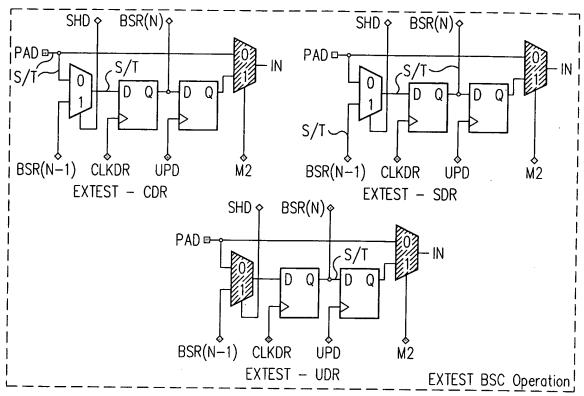


FIG. 13

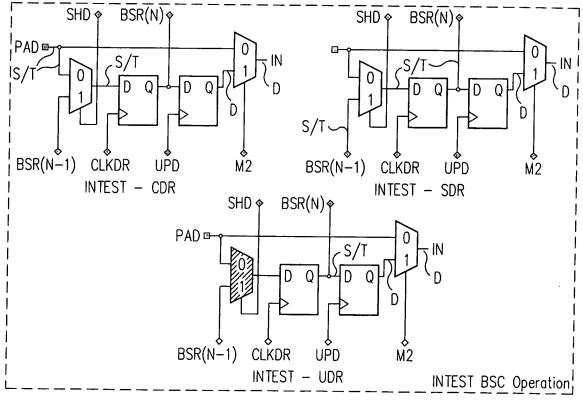
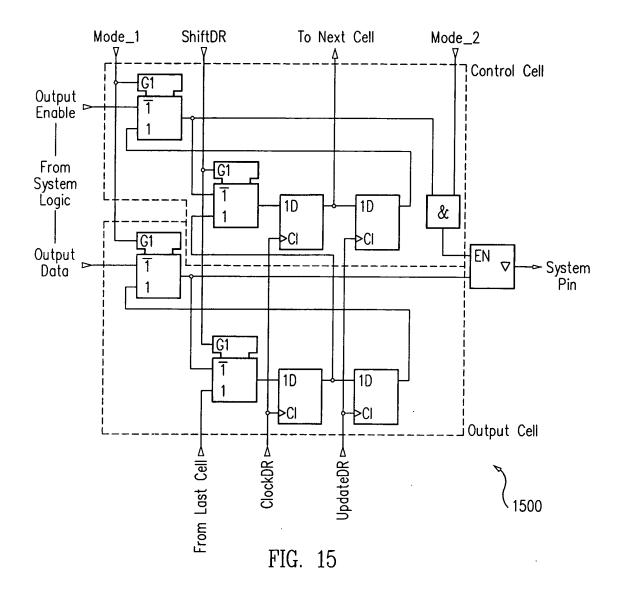


FIG. 14

Clock Generator With Skew Control
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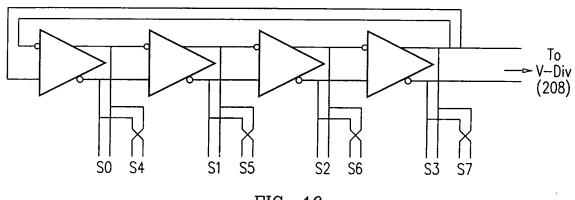
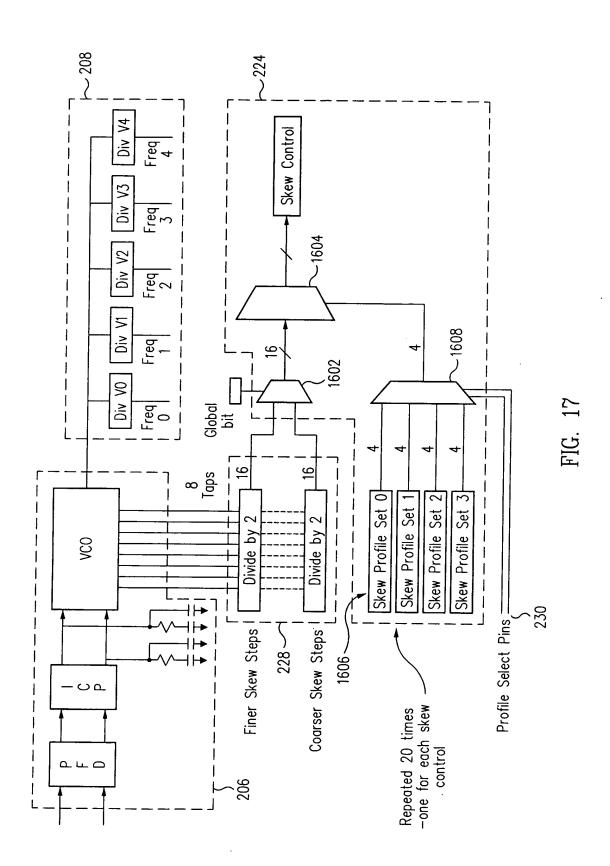
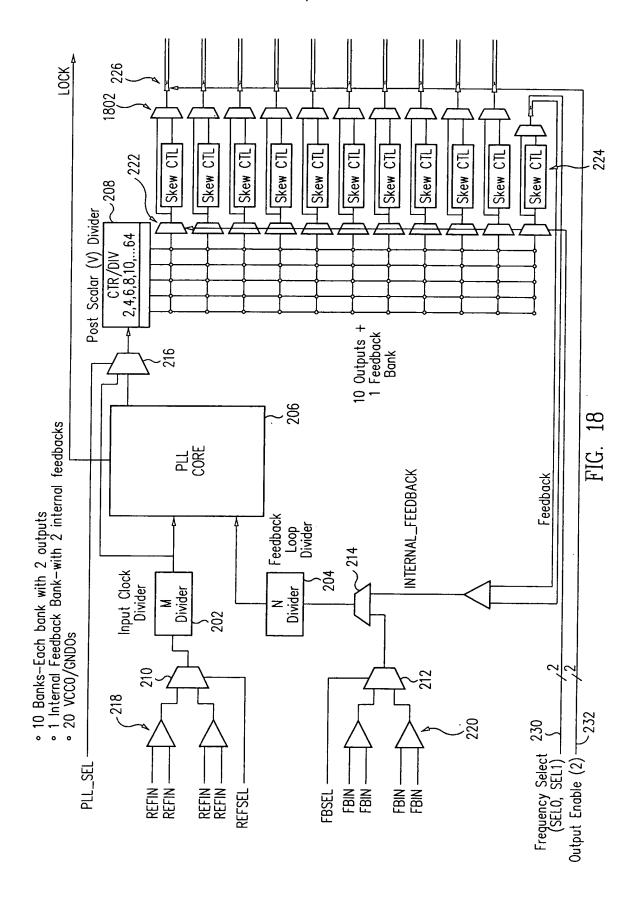


FIG. 16

Clock Generator With Skew Control
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